

IN THE CLAIMS

1. (Currently amended) ~~An image input system A~~
semiconductor integrated circuit device for use with a solid
state image pickup device, the semiconductor integrated
circuit device comprising:

~~a solid state image pickup device and a preprocessor~~
performing a correlated double sampling amplifier which
receives an image amplification on an output signal from
[[of]] the solid state image pickup device and ~~outputting a~~
~~video signal, wherein the preprocessor includes a correlated~~
~~double sampling amplifier outputting signal information~~
~~corresponding to~~ amplifies a difference voltage between a
black level in a feedthrough period of the solid state image
pickup device and a signal level in a charge signal output
period[[;]],

a gain control amplifier which receives an output signal
from the correlated double sampling amplifier and amplifies
the output signal from the correlated double sampling
amplifier, an A/D converter which receives an output signal
from the gain control amplifier and converts the output signal
from the gain control amplifier to a digital video signal, and

an offset cancelling circuit which applies means for
applying an offset cancelling voltage for cancelling an offset

voltage corresponding to an offset in regard to an output ~~the difference voltage between the black level in the feedthrough period of the solid state image pickup device in the state where the solid state image pickup device is optically interrupted and the signal level in the charge signal output period to an input terminal of the correlated double sampling amplifier, and the correlated double sampling amplifier performs cancellation between the offset voltage and the offset cancelling voltage as signal components of polarities opposite to each other.~~

2-8. (Canceled)

9. (New) A semiconductor integrated circuit device according to Claim 1,

wherein the offset voltage corresponds to a difference voltage between the black level in the feedthrough period of the solid state image pickup device in the state where the solid state image pickup device is optically interrupted and the signal level in the charge signal output period in the state where the solid state image pickup device is optically interrupted, and

wherein the output of the solid state image pickup device comprises a source follower.

10. (New) A semiconductor integrated circuit device according to Claim 1,

wherein the offset canceling voltage is programmable from an external device.

11. (New) A semiconductor integrated circuit device according to Claim 1, further comprising:

a correction circuit which detects the difference voltage between the black level in the feedthrough period of the solid state image pickup device in the state where the solid state image pickup device is optically interrupted and the signal level in the charge signal output period where the solid state image pickup device is optically interrupted, generates a feedback clamp voltage based on the detected difference voltage, and then applies the feedback clamp voltage to a signal line between the correlated double sampling amplifier and the A/D converter.